

Abstract Of The Disclosure

A multiplexer circuit (100) has at least two input channels (IN_0 , IN_1) and an output channel (2). Each input channel (IN_0 , IN_1) has a first transmission gate (FT_0 , FT_1) and a second transmission gate (ST_0 , ST_1). The transmission gate can be switched by a select signal ($SELECT_0$, $SELECT_0$; $SELECT_1$, $SELECT_1$) for connecting the input channel (IN_0 , IN_1) to the output channel (2). At least one of the input channels (IN_0 , IN_1) has a bypass circuit for preventing a current flowing through the first transmission gate (FT_0 , FT_1) from reaching the other input channel.